

FIG. 1

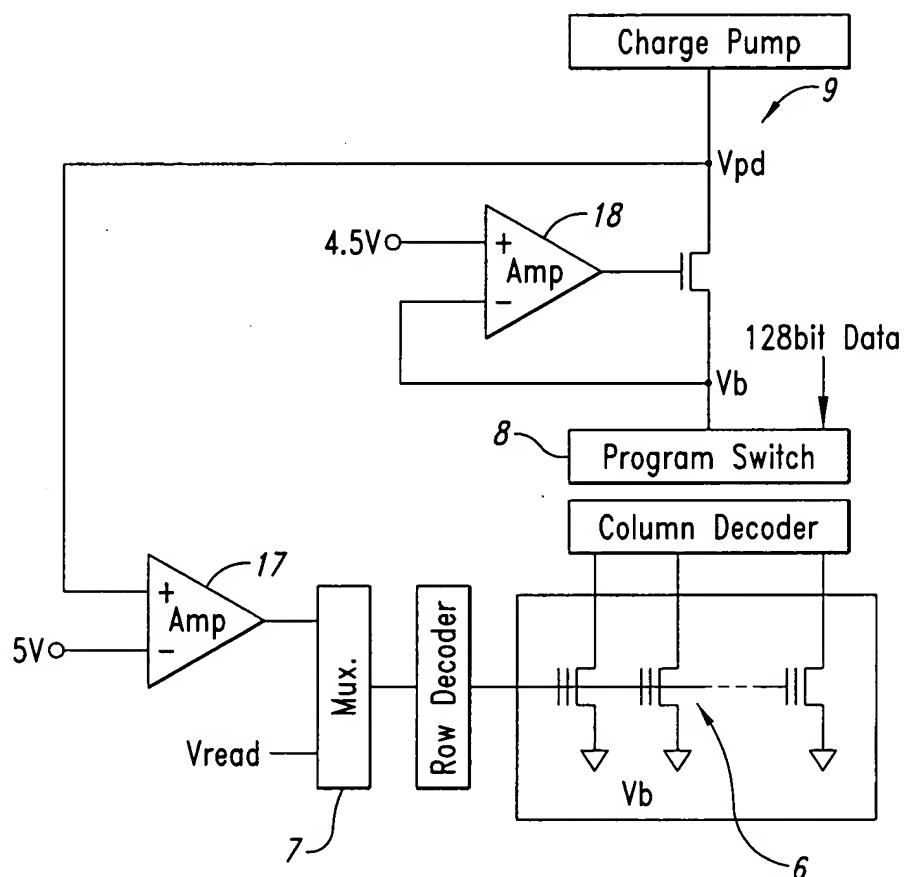


FIG. 2

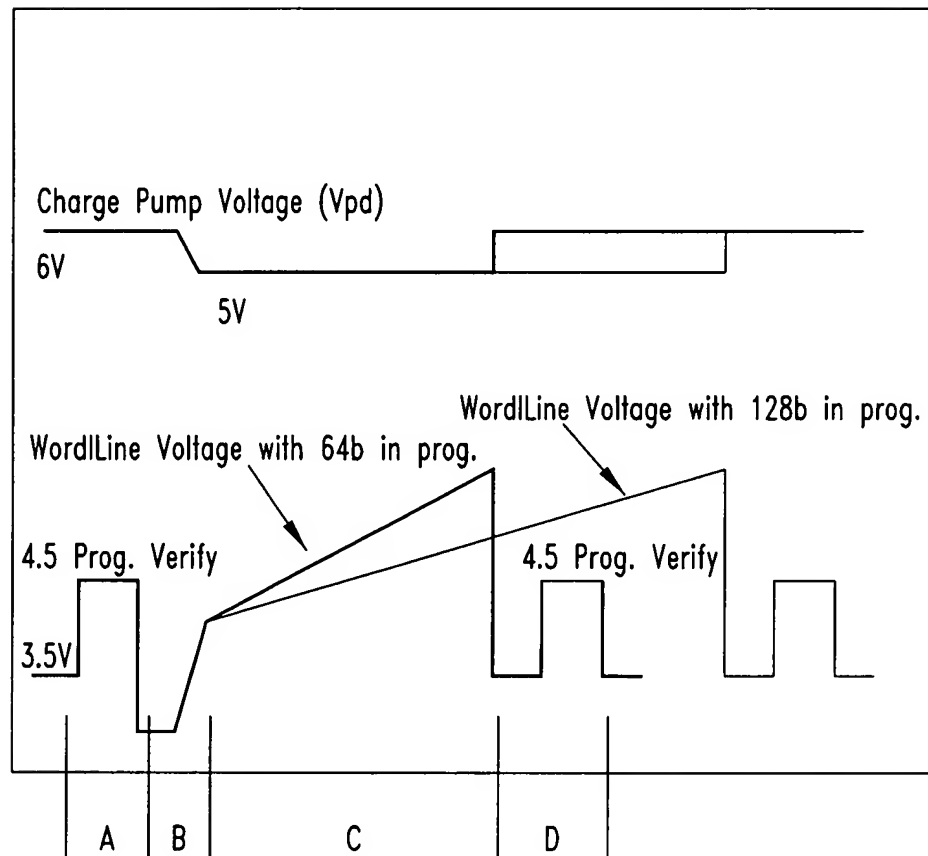


FIG. 3

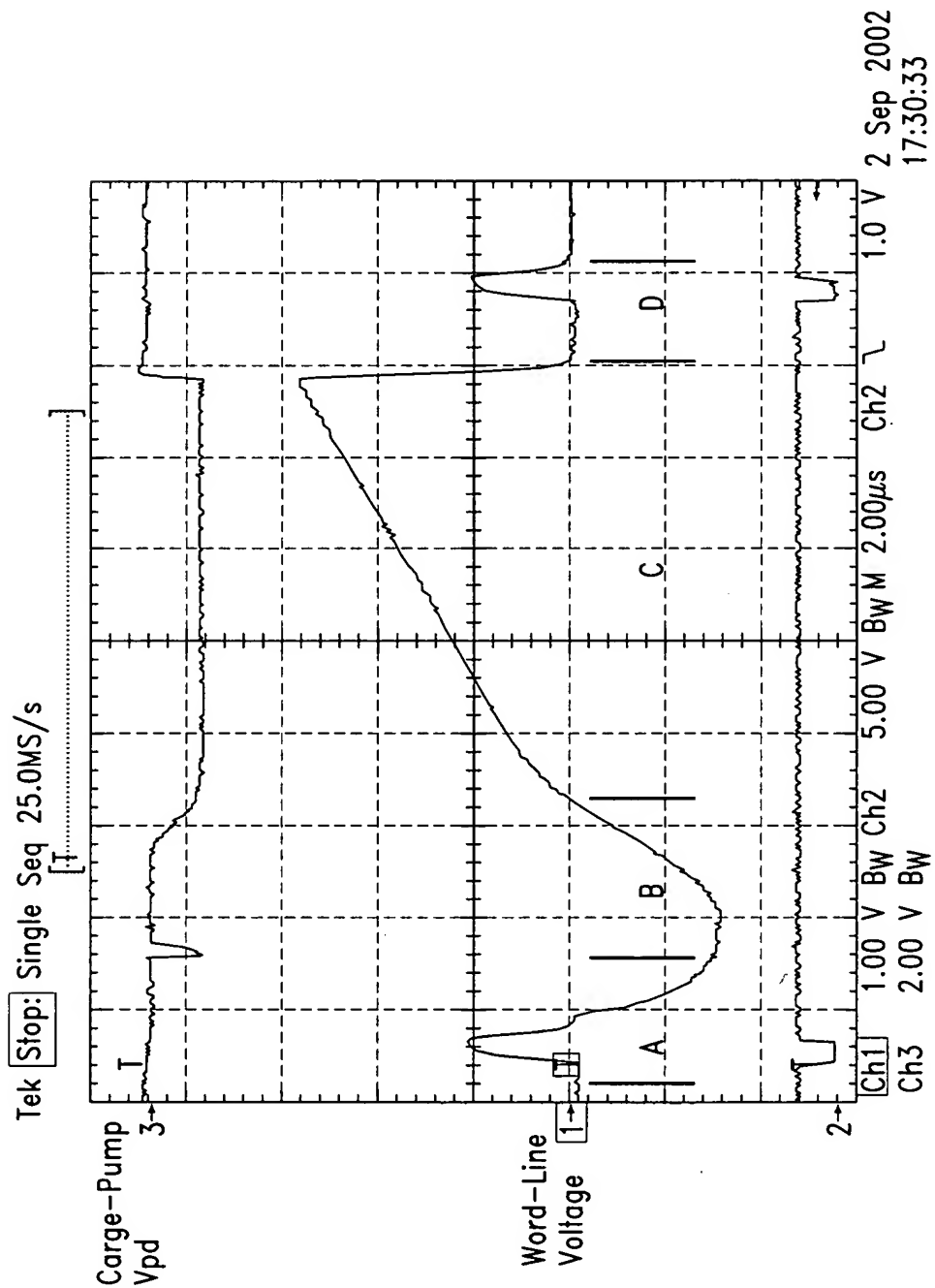


FIG. 4

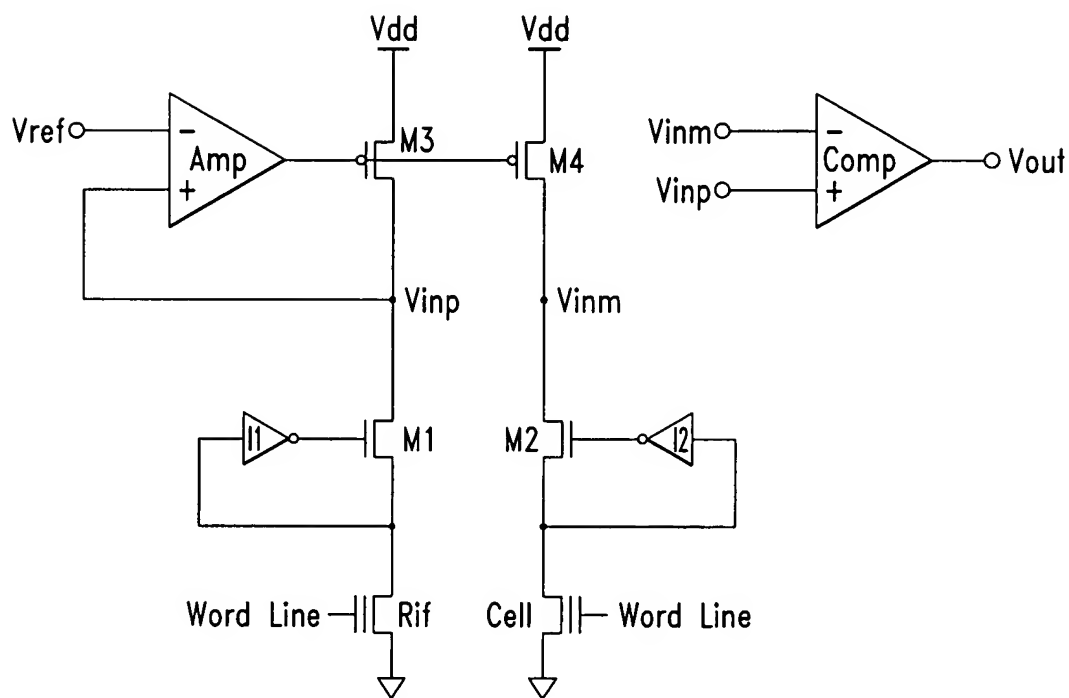


FIG. 5

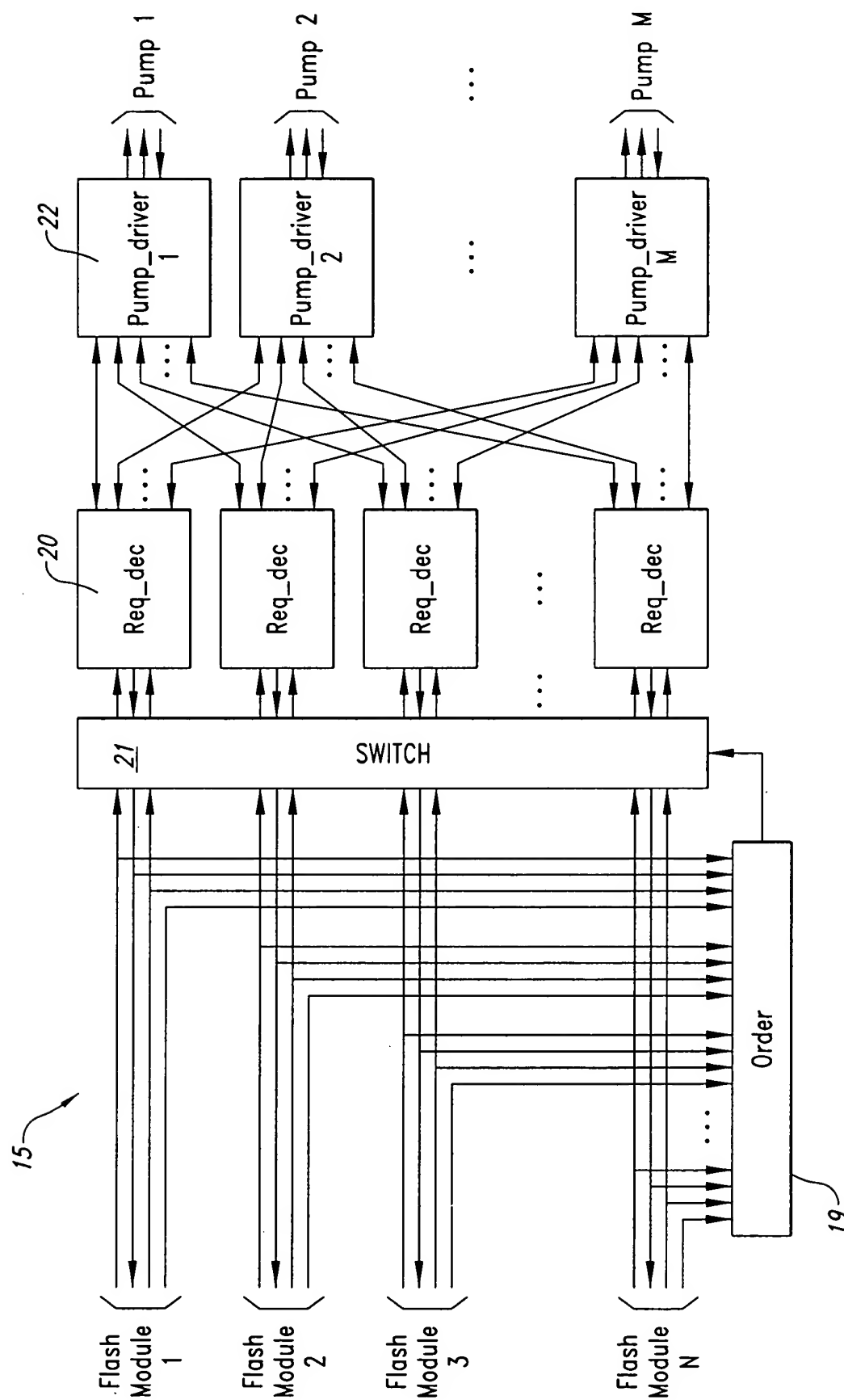


FIG. 6

Title: EMBEDDABLE FLASH MEMORY SYSTEM FOR NON-VOLATILE STORAGE
OF CODE, DATA AND BIT-STREAMS FOR EMBEDDED FPGA CONFIGURATIONS
Inventor(s): Marco Pasotti et al. Express Mail No. EV336613649US Docket No. 856063.762

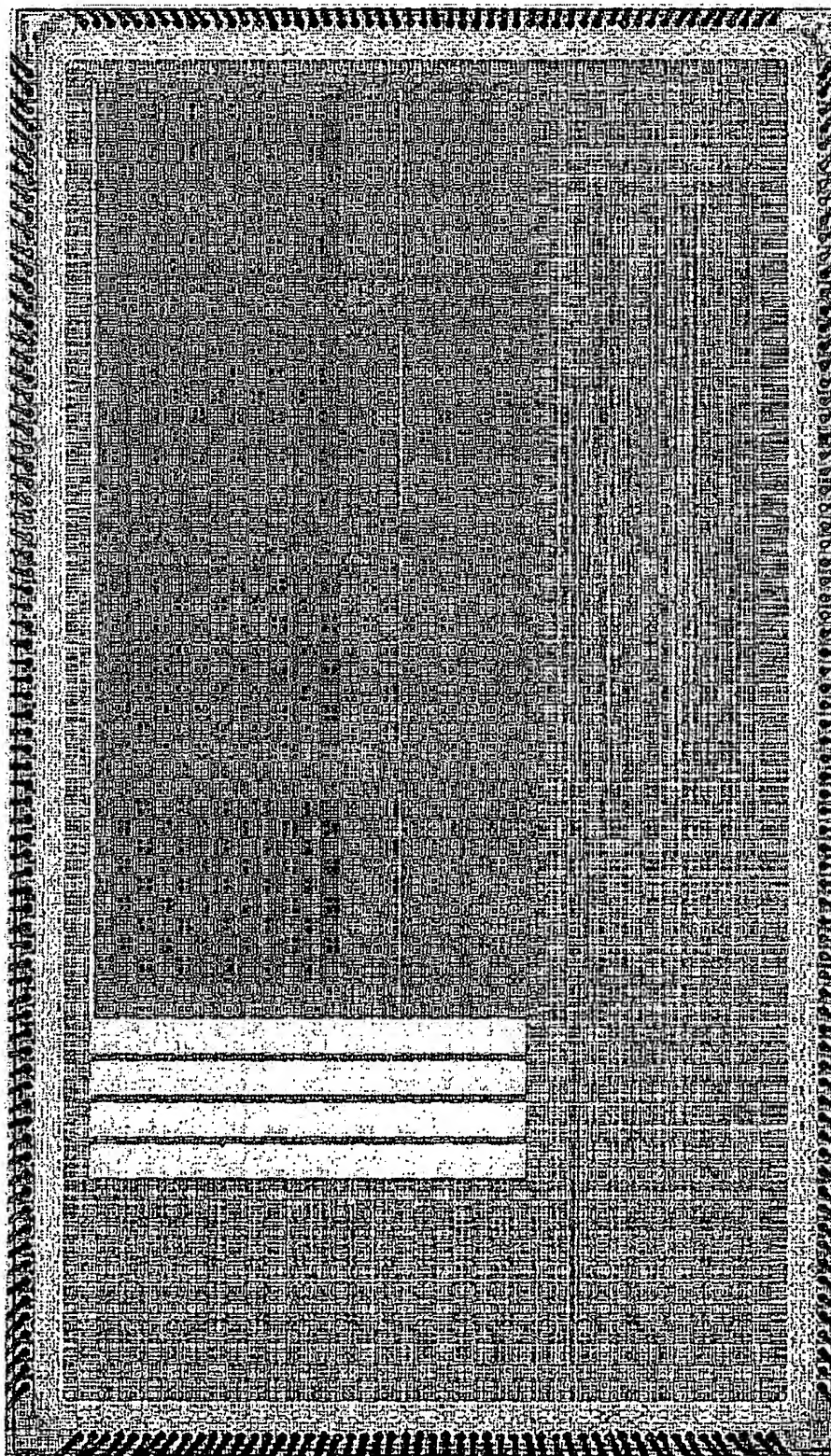


FIG. 7